



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 040301/0487

In re patent application of

Nubuo FUDANUKI

Serial No. 08/997,035

Group Art Unit: 2819

Filed: December 23, 1997

Examiner: D. Le

For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH MIXED GATE ARRAY AND STANDARD CELL

AMENDMENT AND REQUEST FOR RECONSIDERATION
UNDER 37 C.F.R. § 1.111

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In reply to the Office Action mailed July 27, 1999, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 2 and 21 without prejudice or disclaimer.

Please amend the claims 1 and 20 as follows:

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1. (Amended) A semicustom integrated circuit comprising:

(a) a plurality of cell rows, in each row a plurality of standard cell are arranged, the standard cells being configured as rectangular pattern regions having a predetermined height, and different widths so that the standard cells include first and second type cells; and